

REMARKS

Claims 30-69 are pending in this application, with claims 30, 35, 40, 45, 49, 53, 58 and 64 being independent. Claims 32-34, 37-39 and 42-44 have been previously withdrawn. Claims 53-69 have been added. Support for the new claims may be found in the application at least at pages 28-32 and at Figs. 15-18. No new matter has been added.

Independent claims 30, 35, 45, and 49, and their dependent claims 31, 36, 46-48 and 50-52 have been rejected as being unpatentable over Ozawa (U.S. Patent No. 4,608,994) in view of Sawatsubashi (U.S. Patent No. 5,148,301).

Each of independent claims 30, 35 and 45 recites, among other features, a display device including a driver circuit, a sealing material, and a controller for controlling the driver circuit, "wherein the sealing material covers the controller." Applicants request reconsideration and withdrawal of the rejection of claims 30, 35 and 45, and their dependent claims, because neither Ozawa, Sawatsubashi, nor any proper combination of the two describes or suggests the recited controller covered by a sealing material.

As acknowledged by the Examiner, Ozawa does not describe or suggest the specific details of a display device, including the above-referenced controller. The Examiner relies on Sawatsubashi as describing this feature.

Sawatsubashi describes a liquid crystal display device that includes a driving circuit inside a seal boundary of the device. The driving circuit consists of drain line driving circuits 112 for supplying data signals to pixel electrodes 103 and gate line driving circuits 113 for controlling pixel TFTs 104. Col. 4, lines 50-57. The Examiner equates the gate line driving circuits 113 with the recited driver circuit and refers to col. 4, lines 58-67 of Sawatsubashi as describing an integrated circuit that the Examiner equates with the recited controller. Col. 4, lines 58-67, however, merely state that each of the drain line driving circuits 112 and the gate line driving circuits 113 comprises an integrated circuit. Contrary to the Examiner's assertion, however, nothing in this cited portion of Sawatsubashi describes or suggests that this integrated circuit is used for controlling the gate line driving circuit 113. Rather, as described by

Sawatsubashi, the integrated circuit is merely a part of and included in the gate line driving circuit 113.

For at least this reason, applicants request reconsideration and withdrawal of the rejection of claims 30, 35 and 45, and their dependent claims.

Independent claim 49 recites, among other features, a display device including a first substrate, a sealing material, and a CPU formed over the first substrate, "wherein the sealing material covers the CPU." Applicants request reconsideration and withdrawal of the rejection of claim 49, and their dependent claims, because neither Ozawa, Sawatsubashi, nor any proper combination of the two describes or suggests the recited CPU covered by a sealing material.

The Examiner acknowledges that Ozawa does not describe or suggest the specific details of a display device, including the above-referenced CPU. The Examiner relies on Sawatsubashi as describing this feature.

In particular, the Examiner asserts that: "Sawatsubashi et al discloses ...a sealing material (108) formed between the substrates wherein the sealing material covers the driving circuits (controller) (113) and a data signal generating circuit which has to consist of a CPU (see Column 5, Rows 17-32)." Applicants disagree with this assertion. Neither the cited portion nor any portion of Sawatsubashi describes or suggests that the data signal generating circuit has to consist of a CPU. Rather, col. 5, lines 17-32 of Sawatsubashi merely describe the data signal generating circuit 112b as outputting a required voltage signal to the corresponding data line in response to a supplied clock signal when a latched data signal is output.

For at least this reason, applicants request reconsideration and withdrawal of the rejection of claim 49 and its dependent claims.

Independent claim 40 and its dependent claim 41 have been rejected as being unpatentable over Yamano (U.S. Patent No. 4,743,122) in view of Sawatsubashi. Independent claim 40 recites, among other features, a display device including a driver circuit, a sealing material, and a controller for controlling the driver circuit, "wherein the sealing material covers the controller." Yamano does not remedy the failure of Sawatsubashi to describe or suggest the recited controller. Accordingly, for at least the reasons described above, applicants request reconsideration and withdrawal of the rejection of claim 40 and its dependent claim 41 because neither Yamano, Sawatsubashi, nor any proper combination of the two describes or suggests the

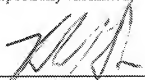
recited controller covered by a sealing material.

New claims 53-69 have been added, including new independent claims 53, 58 and 64. Independent claims 53 and 58 each recite, among other features, a driver circuit, a sealing material, and a controller for controlling the driver circuit, "wherein the sealing material covers the controller." For at least the reasons described above, claims 53 and 58, and their dependent claims, are patentable over the cited art. Independent claim 64 recites, among other features, "a sensor section coupled to the display section in a cordless configuration." Claim 64, and its dependent claims, are patentable over the cited art because none of the cited art, either alone or in combination, describes or suggests at least this feature.

Applicants submit that all claims are in condition for allowance.

The fees in the amount of \$2,360 for the one-month extension of time fee (\$120), the extra claims fee (17 extra = \$850), extra independent claims fee (3 extra = \$600) and the Request for Continued Examination (RCE) fee (\$790) are being paid concurrently herewith on the Electronic Filing System (EFS) by way of Deposit Account authorization. Please apply any other charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,



Roberto J. Devoto
Reg. No. 55,108

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Customer No. 26171
Fish & Richardson P.C.
1425 K Street, N.W. - 11th Floor
Washington, DC 20005-3500
Telephone: (202) 783-5070
Facsimile: (202) 783-2331
/adt
40389084.doc